

Figure 9

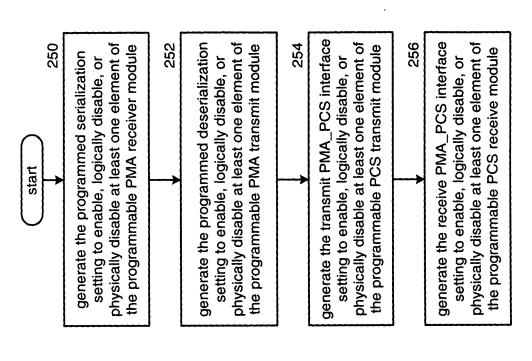


Figure 10

0x00 MASTERBIAS 0x01 TXDIVRATIOLO 0x02 TXDIVRATIOHI 0x03 TXLOOPFILTER 0x04 TXMODECONTROL 0x05 TXOUTPUTLEVEL 0x06 TXOUTPUTMODE	MASTERBIAS TXDIVRATIOLO TXLOOPFILTER TXMODECONTROL TXOUTPUTLEVEL	IBOOST TXREG[1:0]	EW	VCODAC[5:0] TXVSEL[1:0] EMPOFF PRDF TXANASW TXD	TXDIVRATIO[7:0] TXLO TL[1:0] TXVCO PRDRVOFF TXDIGSW	OPFIL	TXBUSWID TXDI TERR[1:0] TXLOO TXVCODAC TXVCODAC TXDOWNLEVEL[3:0]	MASTERBIAS[1:0] TXDIVRATIO[9:8] TXLOOPFILTERC[1:0]	BIAS[1:0] (TIO[9:8]
ما بهمان المستقال المستوالية التي التي التي التي التي التي التي التي	RATIOLO RATIOHI PFILTER CONTROL PUTLEVEL	IBOOST	3[1:0] SLEW	TXVSE EMPOFF TXANASW	TXDIVRA EL[1:0] PRDRVOFF TXDIGSW	TXLOOPFILT TXVCOGAIN 1	TXBUSWID TERR[1:0] TXVCODAC TXDOWNLE	TXDIVR/ TXLOOPFII	(TIO[9:8]
	RATIOHI IPFILTER ECONTROL PUTLEVEL	IBOOST TXRE(3[1:0] SLEW	TXVSE EMPOFF TXANASW	EL[1:0] PRDRVOFF TXDIGSW	TXLOOPFILT	TXBUSWID TERR[1:0] TXVCODAC TXDOWNLE	TXDIVR/ TXLOOPFII	TFRC(1-0)
	PFILTER CONTROL PUTLEVEL	IBOOST	3[1:0] SLEW	TXVSE EMPOFF TXANASW	L[1:0] PRDRVOFF TXDIGSW	TXLOOPFILT	TERR[1:0] TXVCODAC TXDOWNLE	TXLOOPFII	TERC(1:0)
	CONTROL UTLEVEL	TXREC	3[1:0] SLEW	TXVSE EMPOFF TXANASW	EL[1:0] PRDRVOFF TXDIGSW	TXVCOGAIN T	TXVCODAC		5::12::1
	OTLEVEL PUTMODE		SLEW	EMPOFF TXANASW	PRDRVOFF TXDIGSW		TXDOWNLE	And the second s	TXCPI
	OUTMODE	er gemeinte met met etter skrivet skrivet skrivet met met met met met met met met met m		TXANASW	TXDIGSW	H - 00 AULEUS GABLESSERSTENSTENSTENSTENSTENSTENSTENSTENSTENSTEN		:VEL[3:0]	
	J				mentals in the second of the second of the second		TXEMPHLEVEL[3:0]	VEL[3:0]	OF THE THE PERSON AND ADDRESS AND ADDRESS.
	RXDIVRATIOLO				RXDIVRATIO[7:0]	ATIO[7:0]			
0x08 RXDIV	RXDIVRATIOHI	** The state of th			The constitute of the state of	RXDIVRATIO[13:8]	10[13:8]	e ger e n de dese gerpadet entegaliere ; elle litte ;	Company of the Compan
	RXLOOPFILTER				BXL(RXLOOPFILTERR[2:0]	5:0]	RXLOOPFILTERC[1:0]	_TERC[1:0]
0x0A RXN	RXMODE0	RXREG[1:0]	3[1:0]	RXVSEL[1:0]		RXVCOGAIN RXVCODAC	RXVCODAC	RXCPI	RXVCOSW
0x0B BXN	RXMODE1		RXCPGAIN	RXVSELCP[1:0]	.CP[1:0]				-
	RXFEICONTROLO	RXFER[1:0]	ال:1)اح			RXFEI[1:0]	[1:0]	VSELAFE[1:0]	FE[1:0]
0x0D RXFEIC	RXFEICONTROL1				RXFER[9:2]	R[9:2]			
.	<re><re><re><re><re></re></re></re></re></re>	reform the results and results							
0x0F POWER	POWERCONTROL	TXDRVEN	RXEN	TXEN		RXANAEN	TXDIGEN	TXANAEN	BIASEN
0x10-0x3F <res< th=""><td><re><reserved></reserved></re></td><td>a.</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></res<>	<re><reserved></reserved></re>	a.							

Figure 11
PMA memory mapped register 45

GT10_CUSTOM - <10.312	Standard Serial Rate	Encoding	Fabric Interface
	- ≤10.3125 Gbps	Any	908-98
10GE 10GBase-R SONET OC-192	0G Fibre Channel 10.51875 Gbps	64b/66b	64b@159.37MHz
AuroraX AuroraX 10GE 10GBase-R 10GE 10GBase-R SONET OC-192	re Channel 10.51875 Gbps	64b/66b	32b@318.75MHz
AuroraX 10GE 10GBase-R 10GE 10GBase-R SONET OC-192	roraX ≤10.3125 Gbps	64b/66b	64b@156.25MHz
10GE 10GBase-R 10GE 10GBase-R 8 SONET OC-192	roraX ≤10.3125 Gbps	64b/66b	32b@312.5MHz
10GE 10GBase-R SONET OC-192	0GBase-R 10.3125 Gbps	64b/66b	64b@156.25MHz
SONET OC-192	0GBase-R 10.3125 Gbps	64b/66b	32b@312.5MHz
	r OC-192 9.95328 Gbps	None	64b@155.52MHz
GT10_OC192_4 SONET OC-192 9.95328	r OC-192 9.95328 Gbps	None	32b@311.02MHz

Figure 12A

Primitive	Standard	Serial Rate	Encoding	Fabric Interface
GT10_FCXAUI_4	10GFC (XAUI)	3.1875 Gbps	8b/10b	32b@79.6875MHz
GT10_FCXAUI_2	10GFC (XAUI)	3.1875 Gbps	8b/10b	16b@159.375MHz
GT10_FCXAUI_1	10GFC (XAUI)	3.1875 Gbps	8b/10b	8b@318.75MHz
GT10_XAUI_4	10GE (XAUI)	3.125 Gbps	8b/10b	32b@78.125MHz
GT10_XAUI_2	10GE (XAUI)	3.125 Gbps	8b/10b	16b@156.25MHz
GT10_XAUI_1	10GE (XAUI)	3.125 Gbps	8b/10b	8b@312.5MHz
GT10_AURORA_4	Aurora	3.125 Gbps	8b/10b	32b@78.125MHz
GT10_AURORA_2	Aurora	3.125 Gbps	8b/10b	16b@156.25MHz
GT10_AURORA_1	Aurora	3.125 Gbps	8b/10b	8b@312.5MHz

Figure 12B

Primitive	Standard	Serial Rate	Encoding	Fabric Interface
GT10_INFINIBAND_4	InfiniBand	2.5 Gbps	8b/10b	32b@62.5MHz
GT10_INIFINIBAND_2	InfiniBand	2.5 Gbps	8b/10b	16b@125MHz
GT10_INIFINIBAND_1	InfiniBand	2.5 Gbps	8b/10b	8b@250MHz
GT10_3GIO_4	PCI Express	2.5 Gbps	8b/10b	32b@62.5MHz
GT10_3GIO_2	PCI Express	2.5 Gbps	8b/10b	16b@125MHz
GT10_3GIO_1	PCI Express	2.5 Gbps	8b/10b	8b@250MHz
GT10_OC48_4	SONET OC-48	2.488 Gbps	None	32b@77.76MHz
GT10_OC48_2	SONET OC-48	2.488 Gbps	None	16b@155.52MHz
GT10_OC48_1	SONET OC-48	2.488 Gbps	None	8b@311.04MHz

Figure 12C